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## APPLICANTS

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\*\* CONTINUING DATA \*\* *N* *VS*\*\* FOREIGN APPLICATIONS \*\* *N* *VS*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 7	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>US</i>				

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## TITLE

Method and apparatus for timing characterization of integrated circuit designs

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